

**METHOD OF FABRICATING A BIPOLAR JUNCTION TRANSISTOR**

Appl. No.	:	10/707,215	Confirmation No.	1214
Applicant	:	Jing-Horng Gau		
Filed	:	11/26/2003		
TC/A.U.	:	2829		
Examiner	:	Geyer, Scott B		
Docket No.	:	NAUP0536USA0		
Customer No.	:	27765		

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Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

**AMENDMENT**

Sir:

In response to the Office action of 09/22/2004, please amend the  
5 above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which  
begins on page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.